| CERTIFICATE OF Applicant(s): Kwan-You | SIMILE (37 CFR 1.8) | Docket No. YOM-0076 | | | | | | |
|---|---|---------------------------------------|------------------------|--|--|--|--|--|
| Application No. 10/754,389 | Filing Date 1/9/2004 | Examinar Nathan W. HA | Group Art Unit 2814 | | | | | |
| Invention: CHIP LIGHT EMITTING DIODE AND FABRICATION METHOD THEREOF | | | | | | | | |
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| Amendment Transmittal Letter (Sm Entity) (1 pg); I hereby certify that this Response to Restriction Requirement (2 pgs), Ltd. Recognition Under 37 CFR Sec11.9 (b) (Identify type of correspondence) (1 pg) Is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 703-872-9306 | | | | | | | | |
| On May 3, 2 | 2005 | | | | | | | |
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| AMENDMENT TRANSMITTAL LETTER (Small Entity) Applicant(s): Kwan-Young HAN, et al. | | | | | Docket No. YOM-0076 | | | |
|---|--|---|------------|--|------------------------|------------------------|--------------------------|--|
| Application No. 10/754,389 | Filing Date 1/9/2004 | Examiner Nathan W. HA | | Customer N 23413 | lo. | Group Art Unit 2814 | Confirmation No. 9184 | |
| Invention: CHIP LIGHT EMITTING DIODE AND FABRICATION METHOD THEREOF | | | | | | | | |
| | COMMISSIONER FOR PATENTS: | | | | | | | |
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| | atent application proci y credit card. Form PT | essing fees under 37 Ci O-2038 is attached | FR 1.17 | · | | | | |
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| Registration No: See Attached | | | United | United States Postal Service with sufficient postage as first class mall in an envelope addressed to "Commissioner for Patents, P.O. | | | | |
| Cantor Colburn LLP 55 Griffin Road South | | | Box 14 | Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on | | | | |
| Bloomfield, CT 06002 | | | | (Deta) | | | | |
| Telephone No. 860-286-2929 Facsimile No. 860-286-0115 | | | | | | | | |
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Applicant: | Kwan-Young HAN, et al. |)) Group Art Unit: 2814 |
|-----------------|--|-----------------------------|
| Application No: | : |) |
| Filed: | January 9, 2004 |)) Examiner: |
| For: | CHIP LIGHT EMITTING DIODE AND FABRICATION METHOD THEREOF | · . |

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement mailed on April 6, 2005, and in accordance with the provisions under 37 CFR § 1.142, the Applicants submit the following election for further prosecution on the merits:

Election:

The Applicants hereby provisionally elect Group I, claims 1-7, drawn to a semiconductor device, classified in class 257, subclass 99.